

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7169	generat\$3 and ((error signal\$1 or phase error signal\$1) same (phase comparator or phase detector))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 09:35
L2	3	receiv\$3 and (error signal\$1 same first clock circuit same second clock circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 09:36
L3	1	l1 and l2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 09:36
L4	1	generat\$3 same first clock signal same first clock circuit same error signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:35
L5	1	first clock circuit same first clock signal same error signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:36

L6	1	generat\$3 and ((first clock signal) same (error signal) same (first clock circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:37
L7	32	generat\$3 same first clock signal same first clock circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:38
L8	21	generat\$3 same second clock signal same second clock circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:38
L9	142720	error signal\$1 or error clock signal\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:38
L10	18	17 and 18	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:38
L11	3	19 and 110	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:39

L13	1	1 and l11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 10:39
L14	194	first clock circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:16
L15	251	second clock circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:17
L16	13016	first clock signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:17
L17	12634	second clock signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:17
L18	142718	error signal or error clock signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:17

L19	137	14 and I15	US:PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:17
L20	61	19 and I16 and I17	US:PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:17
L21	10	20 and I18	US:PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:18
L22	2	21 and I1	US:PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:18
L23	10	21 and (I1 or I2 or I7 or I8 or I9)	US:PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:18
L24	5067	714/699,700,707,798,799,814;713/400,503,500.ccls.	US:PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:20

L25	1	I23 and I24	US-PGPUB; USPAT; USOOCR; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:20
L26	887	clock distribution circuit	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:22
L27	3	I23 and I26	US-PGPUB; USPAT; USOOCR; FPRS; EPO; JPO; DERWENT; BM_TDB	ADJ	ON	2009/03/28 13:22

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